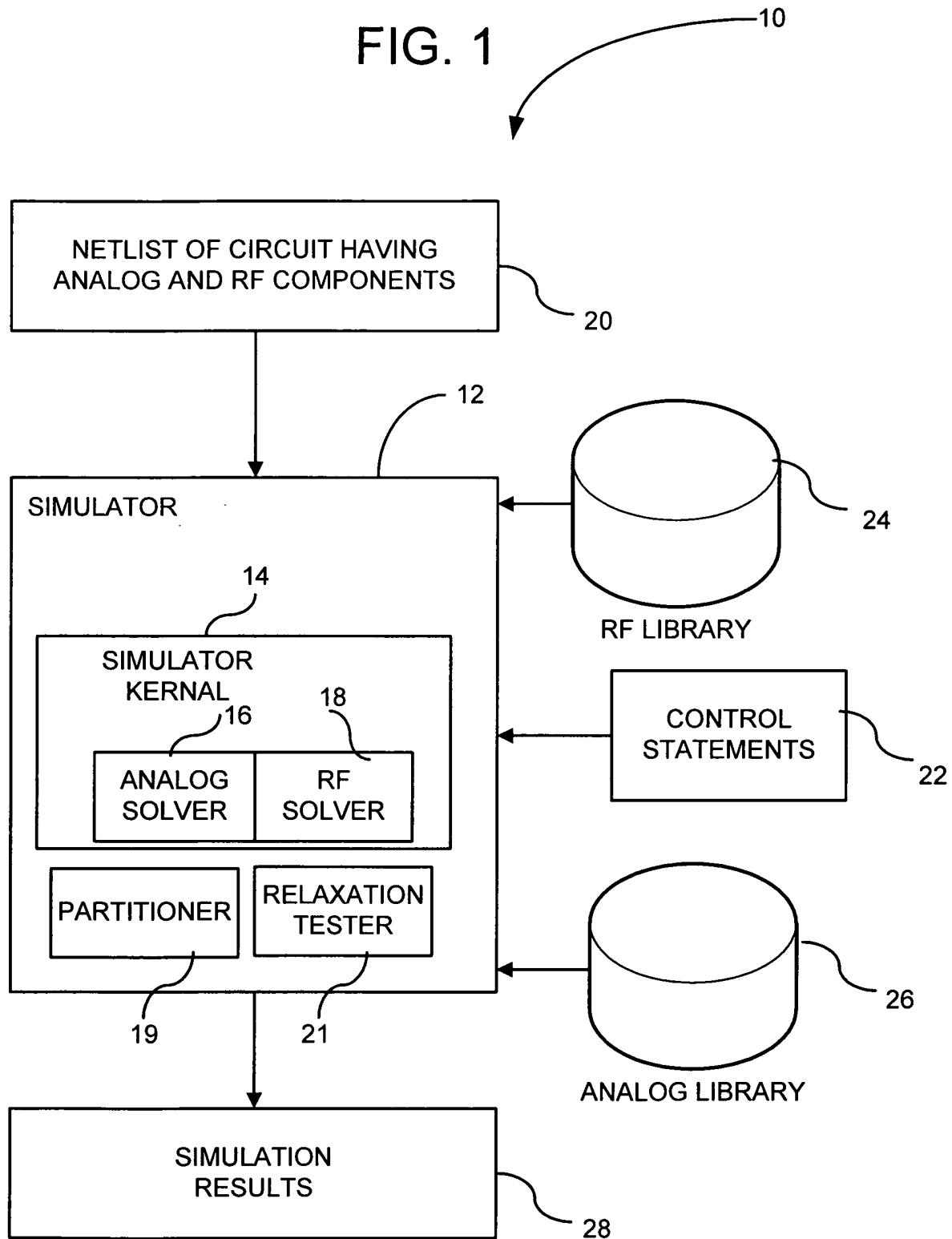


Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

Our Ref. No. 7126-71851-01  
Inventor Name: Pascal Bolcato et al.  
For: MIXED-DOMAIN ANALOG/RF  
SIMULATION  
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FIG. 1

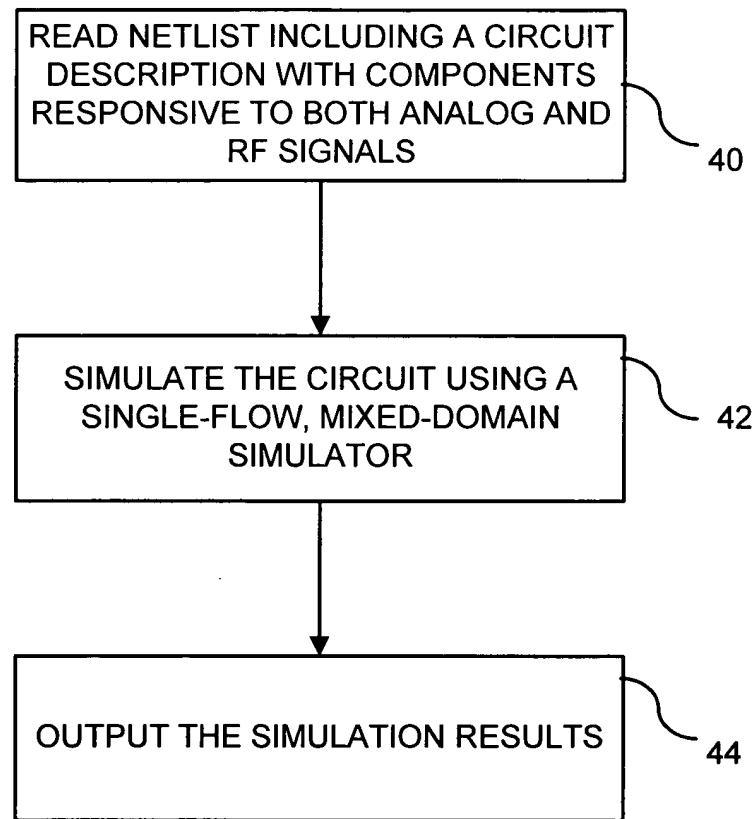


Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

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FIG. 2



Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

Our Ref. No. 7126-71851-01  
Inventor Name: Pascal Bolcato et al.  
For: MIXED-DOMAIN ANALOG/RF  
SIMULATION  
Express Mail No. EV514614461US  
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FIG. 3

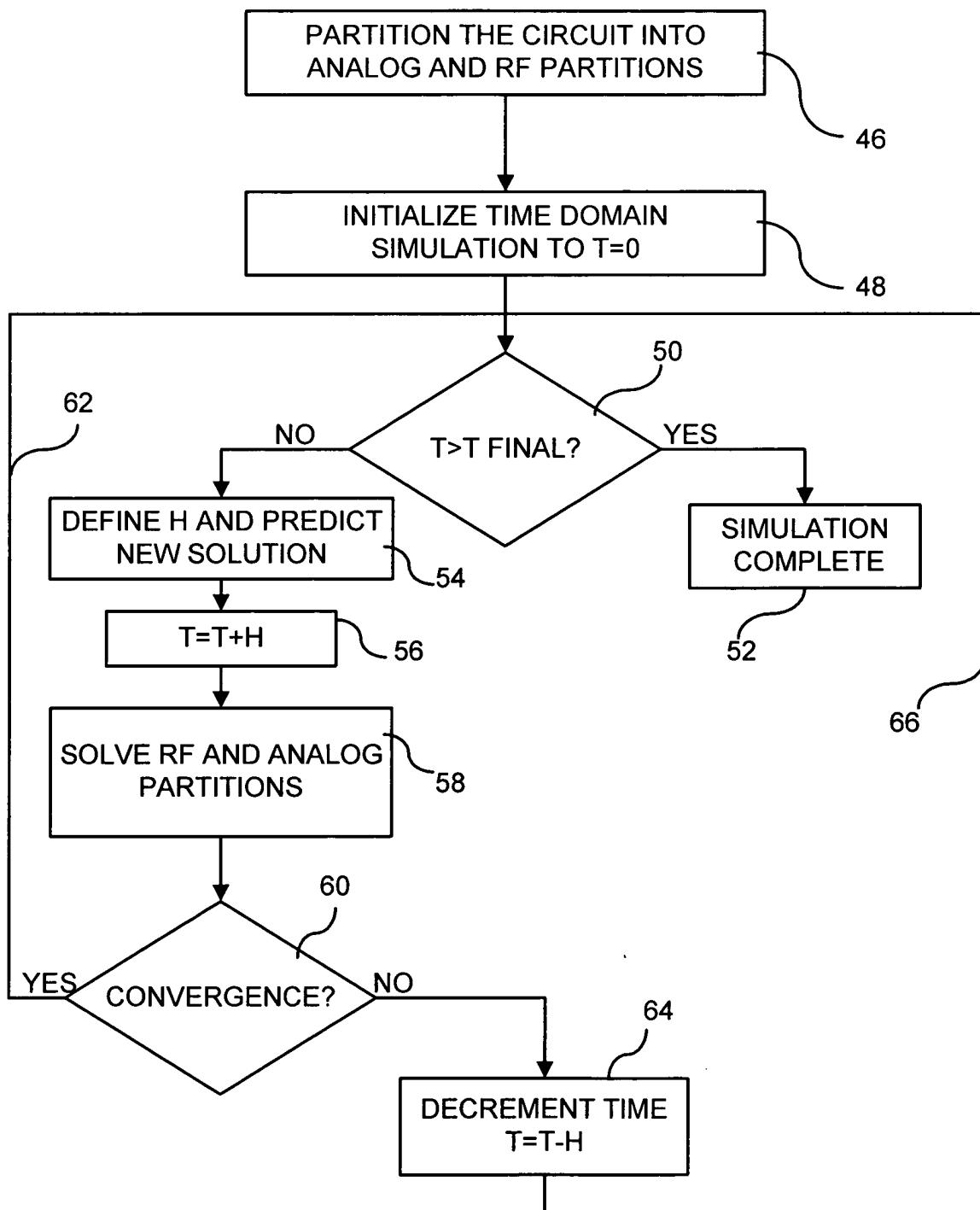
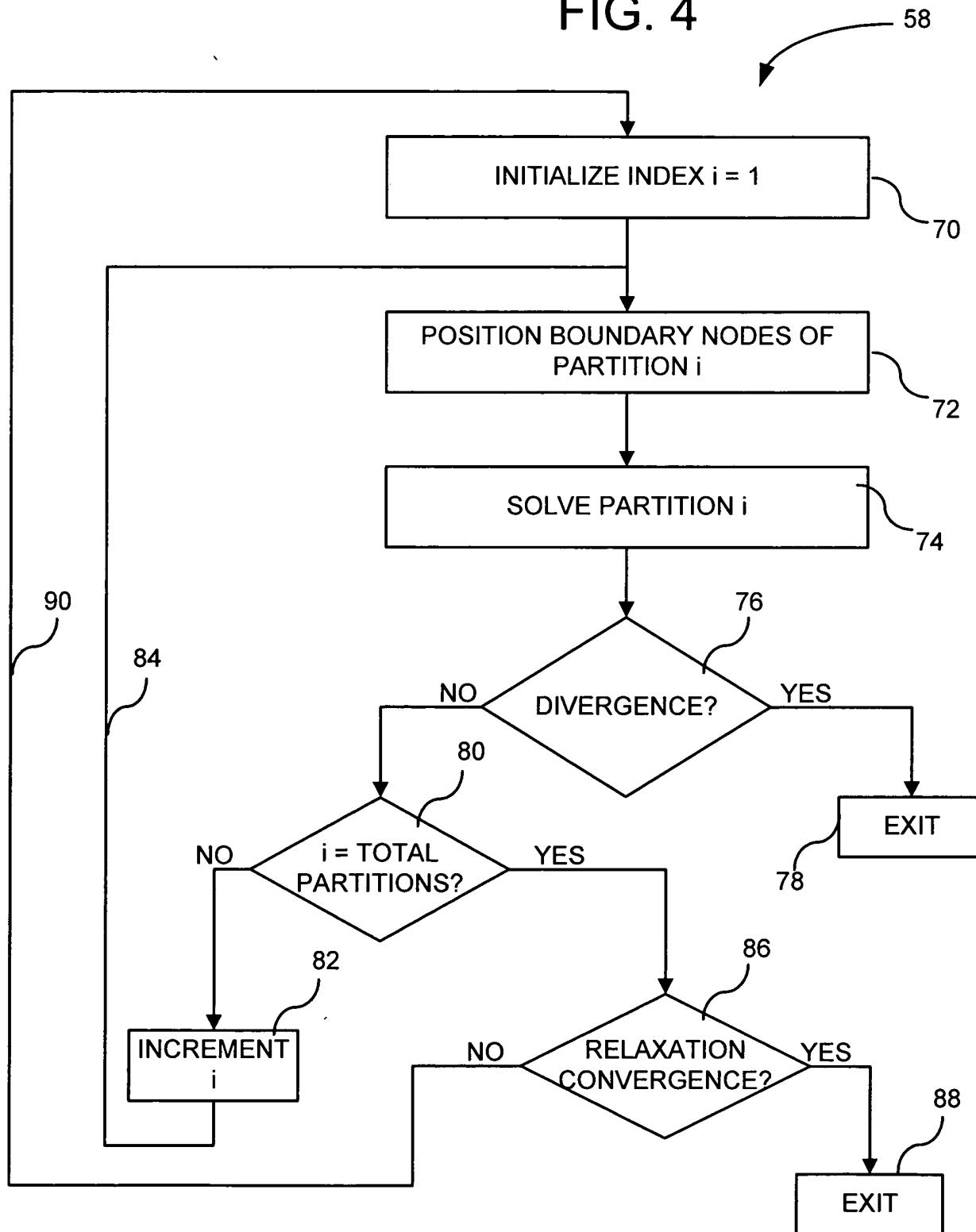


FIG. 4

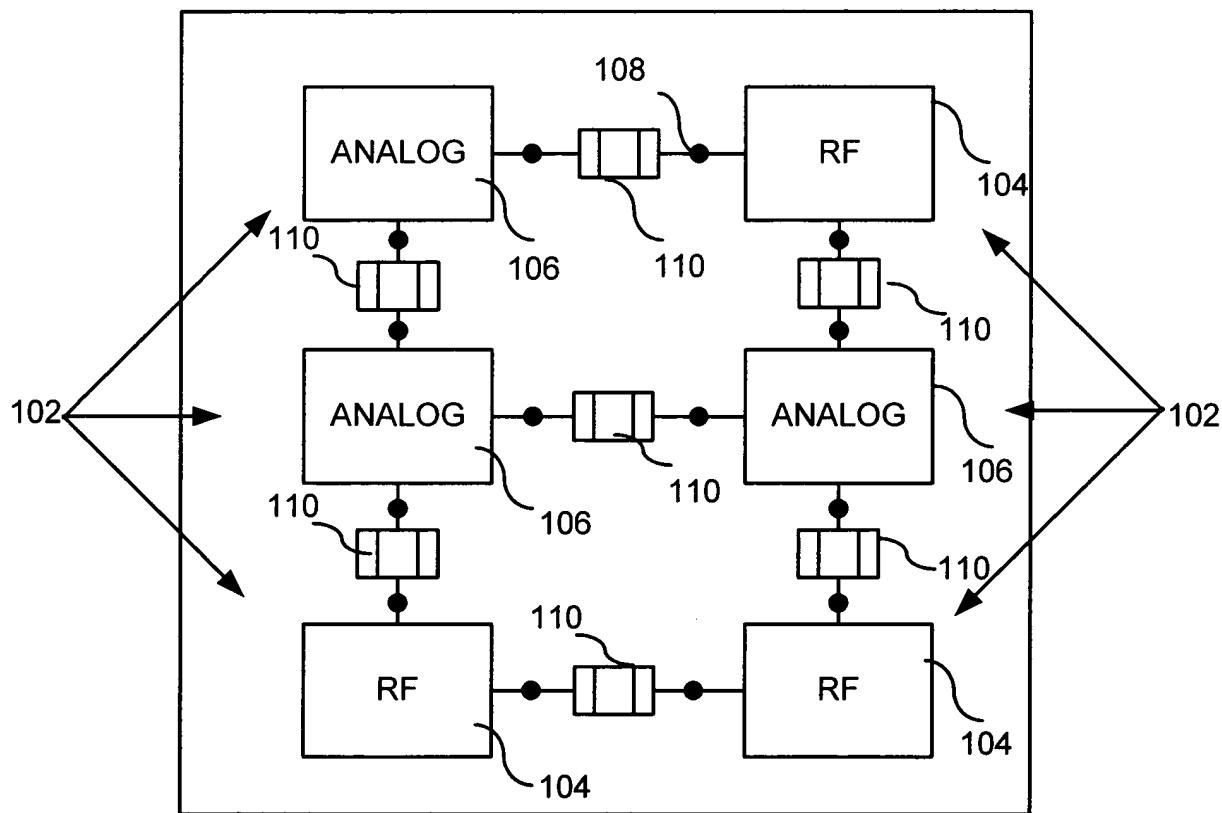


Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

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FIG. 5

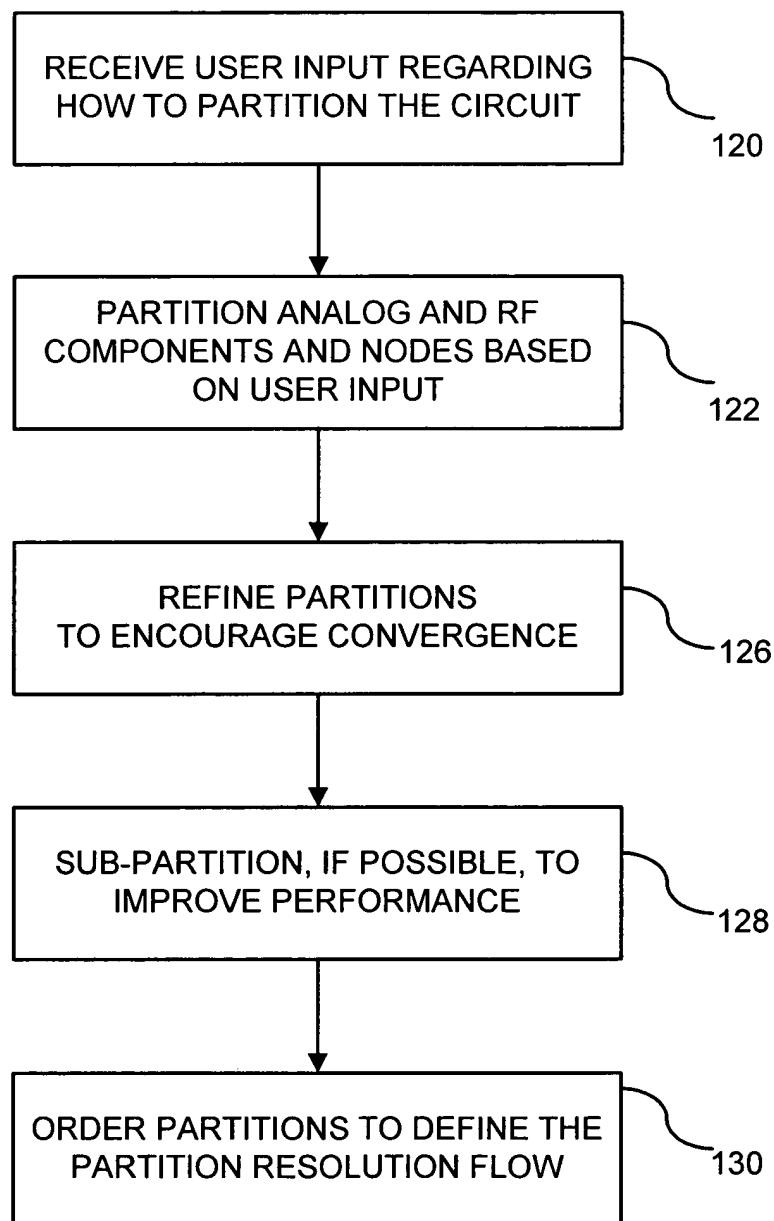


Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

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FIG. 6

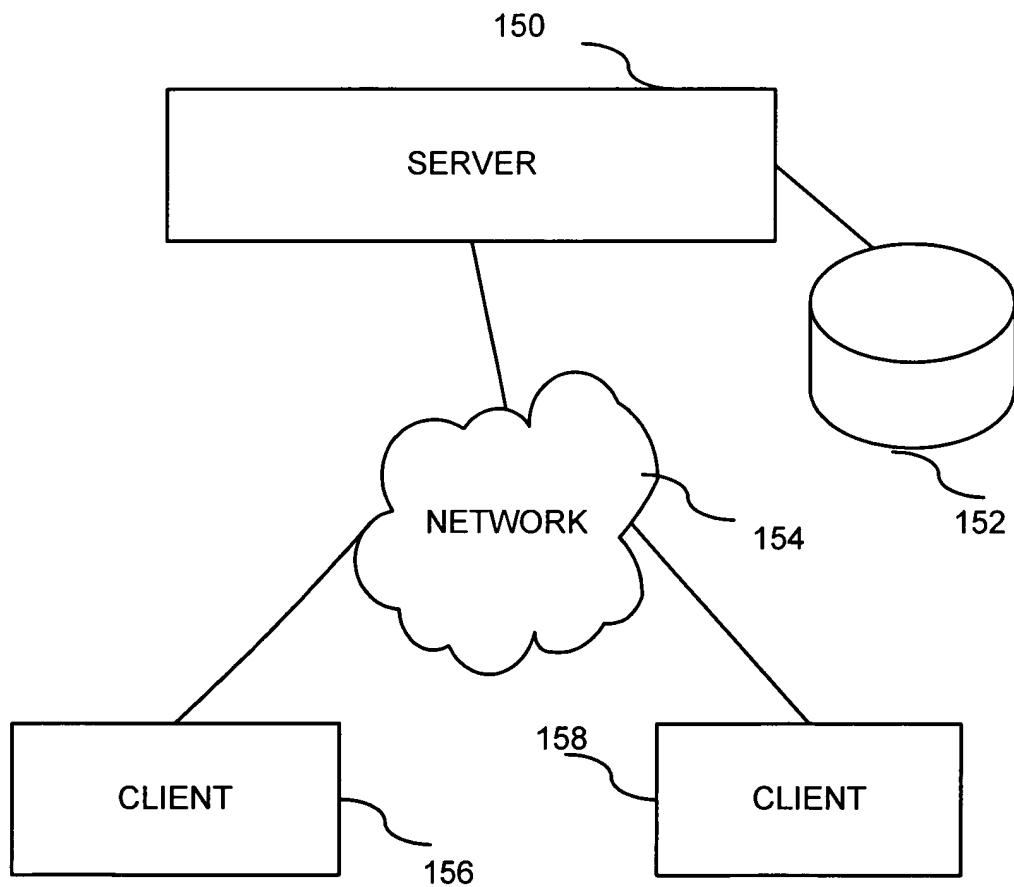


Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

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Inventor Name: Pascal Bolcato et al.  
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FIG. 7



Michael P. Girard  
Klarquist Sparkman LLP  
121 SW Salmon Street  
Suite 1600  
Portland, Oregon 97204  
Telephone: 503/226-7391

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Inventor Name: Pascal Bolcato et al.  
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FIG. 8

